



DEFENSE LOGISTICS AGENCY
DEFENSE SUPPLY CENTER, COLUMBUS
POST OFFICE BOX 3990
COLUMBUS, OH 43216-5000

IN REPLY
REFER TO

DSCC-VQ (VQ-03-003810/Mr. Thomas/614-692-0587)

SUBJECT: Full Q Level Certification and V Level Assembly for MIL-PRF-38535,
FSC 5962

Mr. Ken Buckley
President
Austin Semiconductor, Inc.
8701 Cross Park Drive
Austin, TX 78754-4566

Dear Mr. Buckley:

Austin Semiconductor, Inc. (ASI) has demonstrated to the Defense Supply Center-Columbus (DSCC) that it complies with MIL-PRF-38535, the performance specification used by the Department of Defense for monolithic integrated circuits.

Effective November 5, 2003, the previous Q Certification DSCC-VQ (VQC-98-082) granted to Austin Semiconductor, Inc. by DSCC is superseded by this certification to reflect the current certification status of ASI as approved by their Technical Review Board and documented in ASI's Quality Management Program, ASP 002, Revision B. In addition this certification includes ASI's V level assembly flow that DSCC audited during their February 2003 visit.

The parts that are manufactured using the certified technology flows are being listed on the QML-38535. This will allow Austin Semiconductor, Inc. to mark parts with "Q" or "QML". This designator has been authorized by the Department of Defense for parts that have been produced to a QML specification, (i.e., one which allows less government oversight), the use of world-wide commercial production lines, reduced finished product testing based on statistical process controls (SPC), and other cost advantages.

Testing must be performed using the facilities and methods listed in the letter DSCC-VQC-03-003809, Laboratory Suitability, or at facilities approved by the Austin Semiconductor, Inc. Technical Review Board using its MIL-PRF-38535 Quality Management Program Plan.

This certification is subject to the conditions in DoD 4120.24-M, Defense Standardization Program and SD-6.

Any and all of the facilities mentioned on the enclosure are subject to an audit by the Qualifying Activity at any time. The manufacturer shall be responsible for all audit expenses incurred for the offshore facilities. Offshore facilities are subject to all of the conditions of MIL-PRF-38535, Appendix E.



QPL/QML manufacturers shall notify the qualifying activity immediately after learning of a potential issuance of a GIDEP alert, problem advisory or major quality/reliability problem on their QPL/QML products. Failure to provide prior notification may be grounds for removal from QML-38535.

Finally, it is requested that the following activities be reported promptly to DSCC:

- Changes to certified facilities, process flows, or approved testing subcontractors
- Problem evaluation and a corrective action when:
 - a. A Technology Conformance Inspection (TCI) failure has been validated
 - b. The reliability of shipped parts is questionable.
- Test optimization, including:
 - a. Implementation - paragraph J.3.12, Appendix J, MIL-PRF-38535
 - b. Changing, suspending or canceling a prior test optimization
- Additions or deletions of parts in the QML-38535
- Change of company QML contact or other key QML personnel

This certification is valid until terminated by written notice from the qualifying activity. If warranted, it may be withdrawn by this center at any time.

If you have any questions please contact Mr. Scott Thomas at (614) 692-0587.

Sincerely,



ROBERT P. EVANS
Chief
Sourcing and Qualification Unit

ENCLOSURE

cc:
VQC (Michael Grammens)

Visit us on the web at: www.dsccl.dla.mil/offices/VQ

<u>OPERATION</u>	<u>FACILITY</u>	<u>TECHNOLOGY/OPERATION</u>	<u>LOCATION</u>
Wafer Fabrication:			
	Motorola	SRAM	Austin, TX
	Motorola	ECL	Mesa, AZ
	Samsung	SRAM	Seoul, South Korea
	Texas Instruments	VRAM	Lubbock, TX
Assembly:			
**	Austin Semiconductor	(Custom & Memory Products)	Austin Semiconductor
		- Chip Carriers	8701 Cross Park Dr.
		- Flatpacks	Austin, TX 78754
		- DIPs	
		- PGAs	
		- Metal Cans	
		- J-Lead Chip Carriers	
		- Quad Flatpacks	
	Amkor	Assembly (Memory Products)	Manila, Philippines
	Technology, Inc.	- Chip carriers	
		- Flatpacks	
		- DIPs	
		- Zips	
	Millenium Microtech		Bangkok, Thailand
Test:		As baselined by lab suitability information letter VQC-03-003809	

** V Level assembly capability